Electronic Acknowledgement Receipt					
EFS ID:	1317733				
Application Number:	10707205				
International Application Number:					
Confirmation Number:	1204				
Title of Invention:	SYSTEM AND METHOD TO TEST INTEGRATED CIRCUITS ON A WAFER				
First Named Inventor/Applicant Name:	Lawrence Pileggi				
Customer Number:	24239				
Filer:	Charles L. Moore/Katie Efland				
Filer Authorized By:	Charles L. Moore				
Attorney Docket Number:	361007.000035				
Receipt Date:	16-NOV-2006				
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Time Stamp:	13:46:11				
Application Type:	Utility				

Payment information:

Submitted with Payment	yes
Payment was successfully received in RAM	\$790
RAM confirmation Number	1337
Deposit Account	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
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1	Request for Continued Examination (RCE)	Request_for_Continued_Exa mination.pdf	179735	no	1		
Warnings:							
This is not a l	This is not a USPTO supplied RCE SB30 form.						
Information:							
2	Fee Worksheet (PTO-875)	fee-info.pdf	8201	no	2		
Warnings:							
Information	:						
		Total Files Size (in bytes):	e (in bytes): 187936				

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.